

**REMARKS**

Claims 24-48, which were subject to restriction in parent Application No. 10/191,580, are pending in the captioned Application in which claims 1-23 and 49 are canceled hereby.

Please cancel claims 1-23 and 49 before calculation of the filing fee. This amendment does not narrow the scope of any claim element or limitation and so is not limiting of any claim element or limitation, and Applicant reserves the right to the benefit of the doctrine of equivalents with respect thereto.

The Application paper filed herewith is a photocopy of parent Application 10/191,580 as originally filed, and does not include the amendments as set forth herein.

**Specification:**

The specification is amended in the Title and at pages 1 and 2, as is the Abstract, to consistently reflect the nature of the invention claimed herein, i.e. a method.

The specification is amended at page 1 to expressly claim the priority under 35 U.S.C. §120 of co-pending parent Application 10/191,580 (not yet issued).

The specification is also amended at pages 4 through 7 consistently with the amendment of parent Application 10/191,580. In particular:

The specification is amended at page 4 for clarification regarding the designator 100 which is referred to as an "electronic circuit 100" and an "electronic circuit jumper 100." Both terms are appropriate to and interchangeable with respect to the item 100. Item 100 is a substrate with conductors and an electronic device thereon and so is properly referred to as an "electronic circuit" and also is used to "cross over" or "bridge" or "jump" other conductors (e.g., see Figures 2A-2C, 5 and 6) and so is properly referred to as a "jumper" or more completely as an "electronic circuit jumper." These alternative terms are utilized throughout the specification and are clear to one of ordinary skill in the art.

The specification is amended at pages 4-7 to refer to the designation "D" for the predetermined length shown in amended Figures 2A-2C, 3, and 4. Support therefor is found, for example, in Figures 7A-7C and in the specification description relating thereto.

The Drawing:

Applicants amend Figures 3 and 4 (which show the circuit 100 resulting from Figure 7C) and Figures 2A-2C (which show electronic circuits 100 resulting from Figure 7C on substrates 210S, 210M, 210L) to show the predetermined dimension D thereon, as shown in the revised sheets of formal drawing submitted herewith. This is consistent with the amendment of parent Application No. 10/191,580 and of the specification. Support therefor is found, for example, in Figures 7A-7C and in the specification description relating thereto.

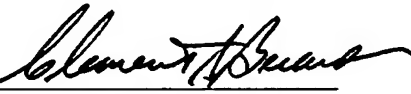
Conclusion:

Applicant respectfully requests that the Application including claims 23-48 be examined, allowed and passed to issuance.

Enclosed is a check in the amount of \$464.00 in payment of the filing fee for the captioned application including claims 23-48 remaining after entry of this amendment. Should any other or additional fee be due, please charge such fee and deposit any refund, to Deposit Account 04-1406.

The Examiner is requested to telephone the undersigned attorney if there is any question or if prosecution of this Application could be furthered by telephone.

Respectfully submitted,  
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